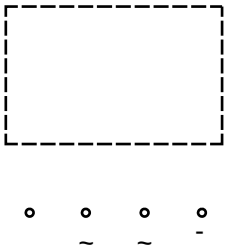


D10JA05A THRU D10 Tin plated leads , solderable per
VRMS V 35



Maximum DC blocking Voltage		VDC	V	50	100	200	400	600	800	1000
Average Rectified Output Current @60H z sine wave , R-load	With heatsink T _c = 110	I _O	A	10.0						
	Without heatsink T _a =25			3.2						
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave,1 cycle, T _j =25		I _{FSM}	A	220						

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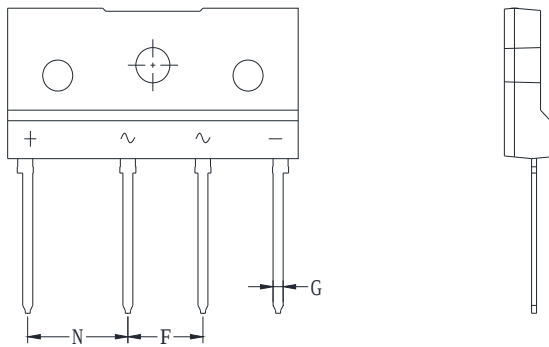
D10JA05A THRU D10JA100A

■Electrical Characteristics $T_a=25$ Unless otherwise specified

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	D10JA05A	D10JA10A	D10JA20A	D10JA40A	D10JA60A	D10JA80A	D10JA100A
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■ Outline Dimensions





D10JA05A THRU D10JA100A

Disclaimer

The information presented in this document is for